

Reference : Advertisement no. 87/Pers./4/2014 dated 02.09.2014

Lists of PH Candidates (Electronics & Communication Engg.)

(Short Listed for Interview)

S.NO	GATE REG. NO.	NAME OF APPLICANT	FATHER's NAME
1.	EC88042S2004	SANTOSH KUMAR SINGH	PANCH LAL SINGH
2.	EC88023S2039	MANDEEP SINGH	IQBAL SINGH
3.	EC55043S2040	PRAGATI KESHARWANI	ANIL KESHARVANI
4.	EC33014S2181	PUSHPENDRA KUMAR	RANVEER SINGH
5.	EC22118S2003	APURVA RAJENDRA PATKAR	RAJENDRA RAMCHANDRA PATKAR
6.	EC33014S2183	PRAVEEN KUMAR DWIVEDI	RAM NARESH DWIVEDI
7.	EC55043S2049	JEEVITA AGRAHARI	DUSHYANT KUMAR AGRAHARI
8.	EC33014S2166	KUNDAN KUMAR SINHA	JAI KUMAR SINHA
9.	EC33014S2128	RAHUL PANJWANI	KRISHAN KUMAR PANJWANI
10.	EC44021S2002	SADDAM HUSSAIN	LATE ANWARUL HAQUE
11.	EC44012S2001	KURMENDRA	LALIT PRASAD
12.	EC44021S2001	Y. RATHAIAH	Y.SATYANARAYANA

All the above listed candidates are to make it convenient to attend the interview to be held on 11.09.2015 from 09:00 hrs onward at our factory premises (Central Electronics Limited, Site – IV, Industrial Area, Sahibabad, near Sahibabad Railway station), in District Ghaziabad (UP).

The candidates are required to bring all certificates in support of age, educational qualification, caste and GATE Score card in original at the time of interview along with one attested copy of all certificates duly attested by Gazetted Officer.

No TA DA will be paid for attending interview to the candidates.

The candidature for interview is subject to scrutiny of all original certificates and the selection of candidate at Graduate Engineer on contract basis is subject to passing B.Tech with minimum 65% marks.

Asstt. General Manager (HRD)